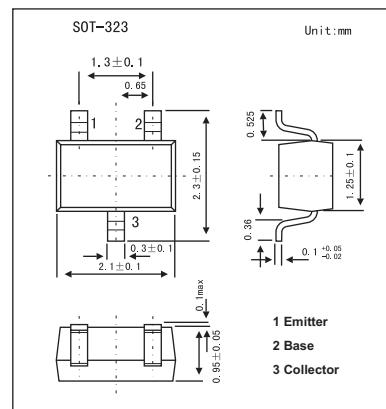
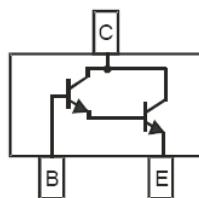


NPN Surface Mount Darlington Transistor

MMSTA13

■ Features

- Epitaxial Planar Die Construction
- Ideal for Medium Power Amplification and Switching
- High Current Gain



■ Absolute Maximum Ratings Ta = 25°C

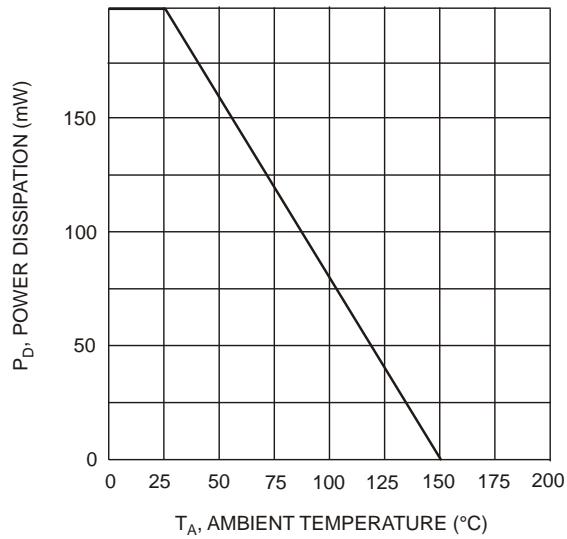
Parameter	Symbol	Rating	Unit
Collector-Base Voltage	V _{CBO}	30	V
Collector-Emitter Voltage	V _{C EO}	30	V
Emitter-Base Voltage	V _{EBO}	10	V
Collector Current	I _C	300	mA
Power Dissipation	P _d	200	mW
Thermal Resistance, Junction to Ambient	R _{θ JA}	625	°C/W
Operating and Storage and Temperature Range	T _j , T _{STG}	-55 to +150	°C

■ Electrical Characteristics Ta = 25°C

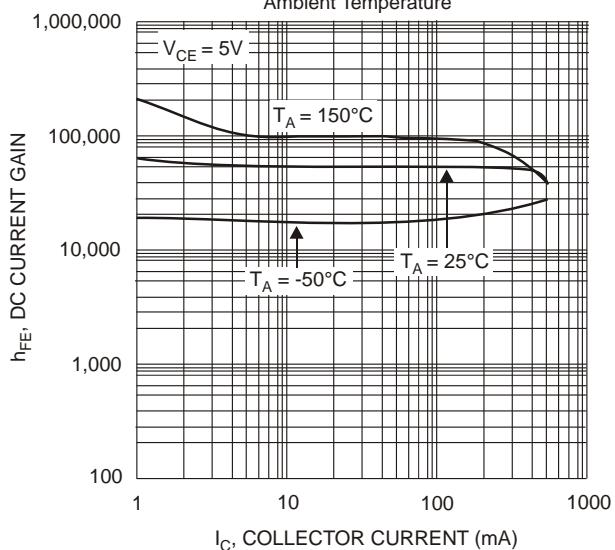
Parameter	Symbol	Testconditons	Min	Typ	Max	Unit
Collector-Base Breakdown Voltage	V _{CBO}	I _C = 100 μA, I _E = 0	30			V
Collector Cutoff Current	I _{cbo}	V _{CB} = 30V, I _E = 0			100	nA
Collector Cutoff Current	I _{EBO}	V _{CE} = 10V, I _C = 0			100	nA
DC Current Gain	h _{FE}	I _C = 10mA, V _{CE} = 5V	5,000			
		I _C = 100mA, V _{CE} = 5V	10,000			
Collector-Emitter Saturation Voltage	V _{CE(sat)}	I _C = 100mA, I _B = 100 μA			1.5	V
Base-Emitter Saturation Voltage	V _{BE(sat)}	I _C = 100mA, V _{CE} = 5.0V			2.0	V
Output Capacitance	C _{obo}	V _{CB} = 10V, f = 1.0MHz, I _E = 0		8.1		pF
Input Capacitance	C _{ibo}	V _{EB} = 0.5V, f = 1.0MHz, I _C = 0		15		pF
Current Gain-Bandwidth Product	f _T	V _{CE} = 5.0V, I _C = 10mA, f = 100MHz	125			MHz

■ Marking

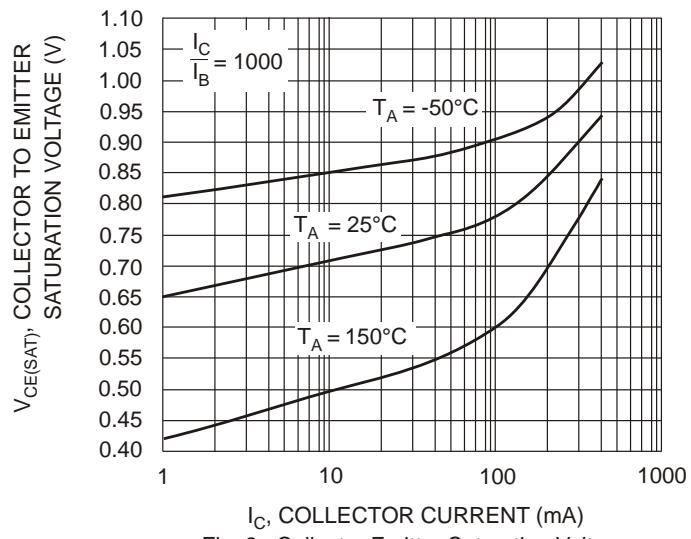
Marking	K2D
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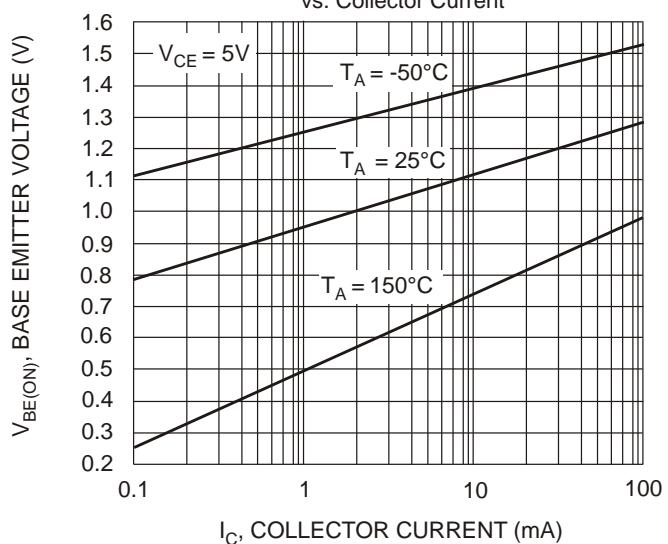
T_A , AMBIENT TEMPERATURE (°C)
Fig. 1, Max Power Dissipation vs
Ambient Temperature



$V_{CE} = 5\text{V}$
Fig. 3, DC Current Gain vs
Collector Current



I_C , COLLECTOR CURRENT (mA)
Fig. 2, Collector Emitter Saturation Voltage
vs. Collector Current



$V_{CE} = 5\text{V}$
Fig. 4, Base Emitter Voltage
vs. Collector Current

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