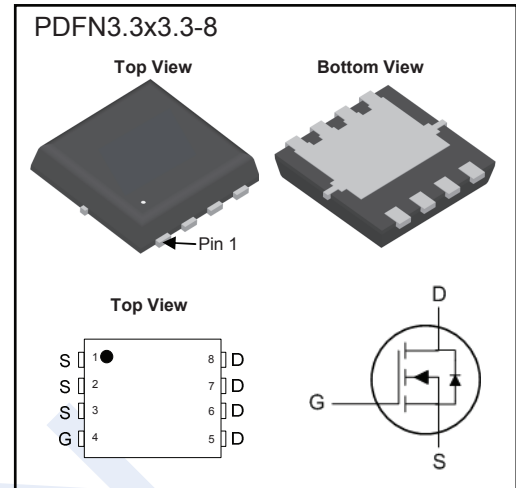


## N-Channel MOSFET

## KI60N03DFN

## ■ Features

- $V_{DS} (V) = 30 V$
- $I_{D(MAX)} = 60 A$
- $R_{DS(ON)}$  (at  $V_{GS} = 10 V$ )  $< 7 m\Omega$

■ Absolute Maximum Ratings ( $T_A = 25^\circ C$  unless otherwise noted)

Parameter		Symbol	Rating	Unit	
Drain-Source Voltage		$V_{DS}$	30	V	
Gate-Source Voltage		$V_{GS}$	$\pm 20$		
Continuous Drain Current, $V_{GS} @ 10V$ (Note 1)	$T_C = 25^\circ C$	$I_D$	60	A	
	$T_C = 100^\circ C$		38		
	$T_A = 25^\circ C$		10s		20
			Steady State		12.7
	$T_A = 70^\circ C$		10s		16
			Steady State		10.2
Pulsed Drain Current (Note 2)		$I_{DM}$	180		
Avalanche Current		$I_{AS}$	48		
Single Pulse Avalanche Energy (Note 3)		$E_{AS}$	252	mJ	
Power Dissipation (Note 4)	$T_C = 25^\circ C$	$P_D$	37	W	
	$T_A = 25^\circ C$		10s		4.2
			Steady State		1.67
Thermal Resistance.Junction- to-Ambient (Note 1)	Steady State	$R_{thJA}$	75	$^\circ C/W$	
	$t \leq 10s$		30		
Thermal Resistance.Junction- to-Case (Note 1)		$R_{thJC}$	3.36		
Junction Temperature		$T_J$	150	$^\circ C$	
Storage Temperature Range		$T_{stg}$	-55 to 150		

## N-Channel MOSFET

## KI60N03DFN

■ Electrical Characteristics (T<sub>J</sub> = 25°C unless otherwise specified)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	I <sub>D</sub> = 250 μA, V <sub>GS</sub> = 0V	30			V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = 24 V, V <sub>GS</sub> = 0 V			1	μA
		V <sub>DS</sub> = 24 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 55 °C			5	
Gate to Source Leakage Current	I <sub>GSS</sub>	V <sub>DS</sub> = 0 V, V <sub>GS</sub> = ±20 V			±100	nA
Gate to Source Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250μA	1.2		2.5	V
Static Drain-Source On-Resistance (Note 2)	R <sub>DS(on)</sub>	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 30 A			7	mΩ
		V <sub>GS</sub> = 4.5 V, I <sub>D</sub> = 15 A			10	
Forward Transconductance	g <sub>FS</sub>	V <sub>DS</sub> = 5 V, I <sub>D</sub> = 30 A		43		S
Input Capacitance	C <sub>iss</sub>	V <sub>GS</sub> = 0 V, V <sub>DS</sub> = 15 V, f = 1 MHz		2295	3213	pF
Output Capacitance	C <sub>oss</sub>			267	374	
Reverse Transfer Capacitance	C <sub>rss</sub>			210	294	
Gate Resistance	R <sub>g</sub>	V <sub>GS</sub> = 0 V, V <sub>DS</sub> = 0 V, f = 1 MHz		1.6	2.8	Ω
Single Pulse Avalanche Energy (Note 5)	E <sub>AS</sub>	V <sub>DD</sub> =25V, L=0.1mH, I <sub>AS</sub> =24A	63			mJ
Total Gate Charge	Q <sub>g(4.5V)</sub>	V <sub>GS</sub> = 4.5V, V <sub>DS</sub> = 15 V, I <sub>D</sub> = 15 A		20	28	nC
Gate Source Charge	Q <sub>gs</sub>			7.6	10.6	
Gate Drain Charge	Q <sub>gd</sub>			7.2	10.1	
Turn-On DelayTime	t <sub>d(on)</sub>	V <sub>GS</sub> = 10V, V <sub>DD</sub> = 15 V, R <sub>G</sub> = 3.3 Ω, I <sub>D</sub> = 15A		7.8	15.6	ns
Turn-On Rise Time	t <sub>r</sub>			15	27	
Turn-Off DelayTime	t <sub>d(off)</sub>			37.3	75	
Turn-Off Fall Time	t <sub>f</sub>			10.6	21.2	
Body Diode Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = 30 A, di/dt = 100 A/μs		14		nA
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>			5		
Maximum Body-Diode Continuous Current (Note 1,6)	I <sub>S</sub>	V <sub>G</sub> =V <sub>D</sub> =0V, Force Current			60	A
Pulsed Source Current (Note 2,6)	I <sub>SM</sub>				180	
Diode Forward Voltage (Note 2)	V <sub>SD</sub>	V <sub>GS</sub> = 0 V, I <sub>S</sub> = 1 A			1	V

Notes:

- 1.The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper.
- 2.The data tested by pulsed, pulse width ≦ 300us, duty cycle ≦ 2%
- 3.The EAS data shows Max. rating. The test condition is V<sub>DD</sub>=25V, V<sub>GS</sub>=10V, L=0.1mH, I<sub>AS</sub>=48A
- 4.The power dissipation is limited by 150°C junction temperature
- 5.The Min. value is 100% EAS tested guarantee.
- 6.The data is theoretically the same as I<sub>D</sub> and I<sub>DM</sub>, in real applications, should be limited by total power dissipation.

# N-Channel MOSFET

## KI60N03DFN

### Typical Characteristics

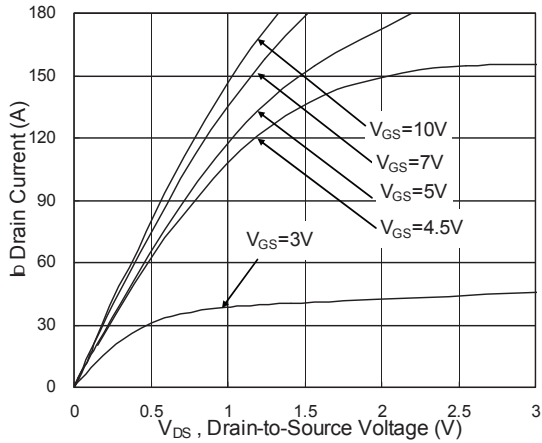


Fig.1 Typical Output Characteristics

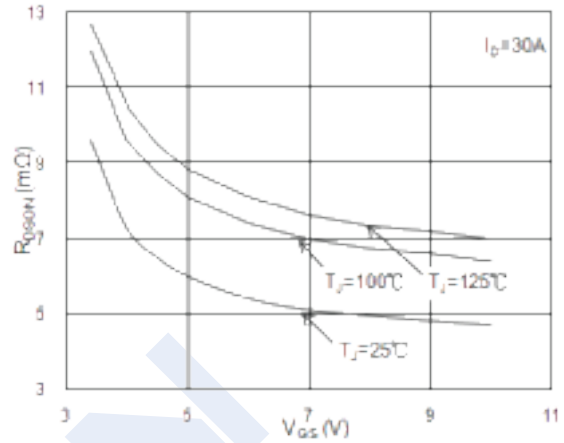


Fig.2 On-Resistance vs. G-S Voltage

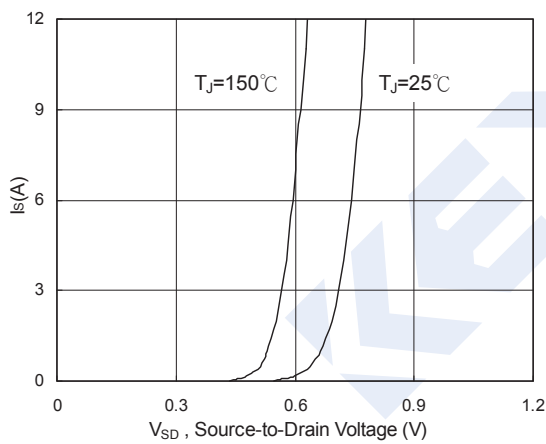


Fig.3 Forward Characteristics Of Reverse

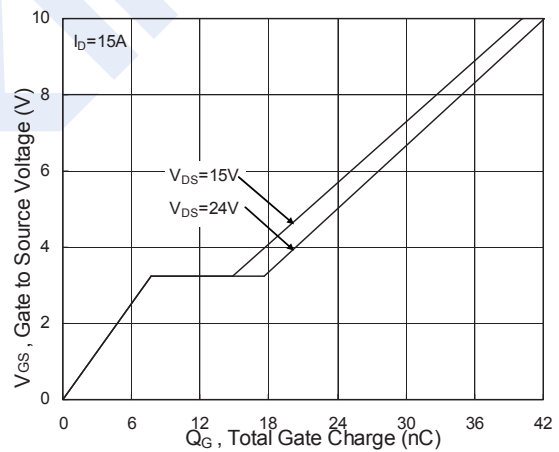


Fig.4 Gate-Charge Characteristics

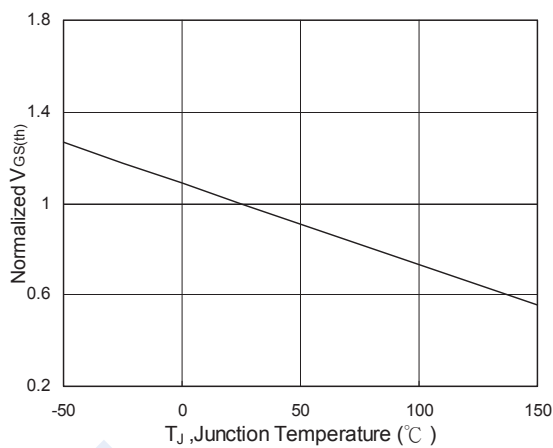


Fig.5 Normalized  $V_{GS(th)}$  vs.  $T_J$

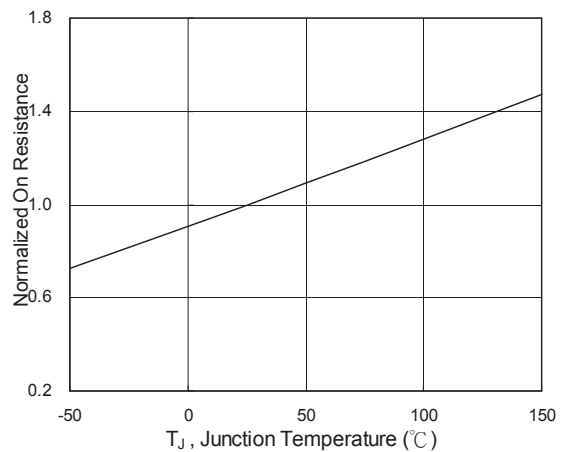


Fig.6 Normalized  $R_{DS(on)}$  vs.  $T_J$

# N-Channel MOSFET

## KI60N03DFN

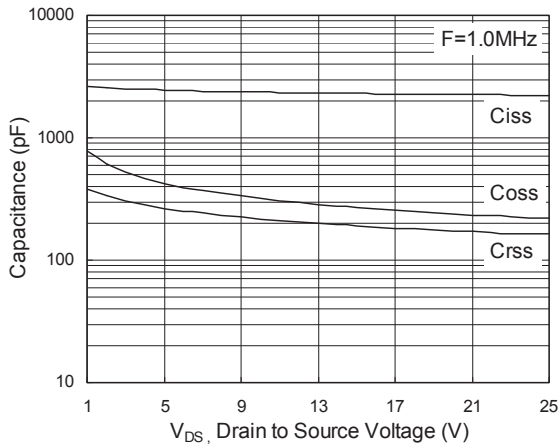


Fig.7 Capacitance

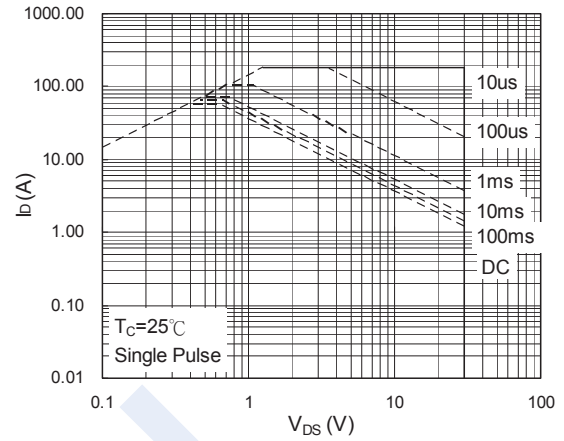


Fig.8 Safe Operating Area

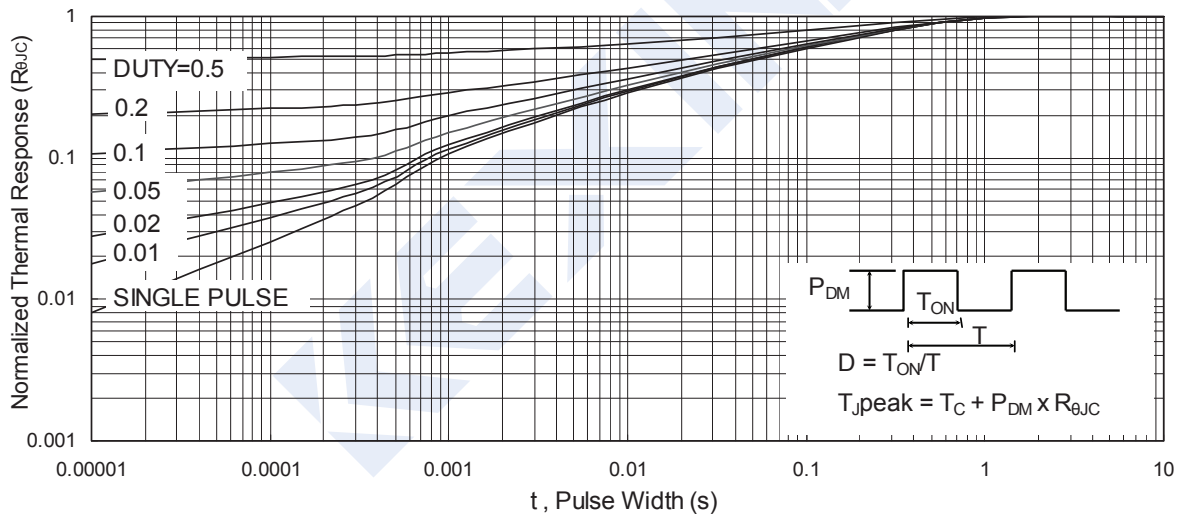


Fig.9 Normalized Maximum Transient Thermal Impedance

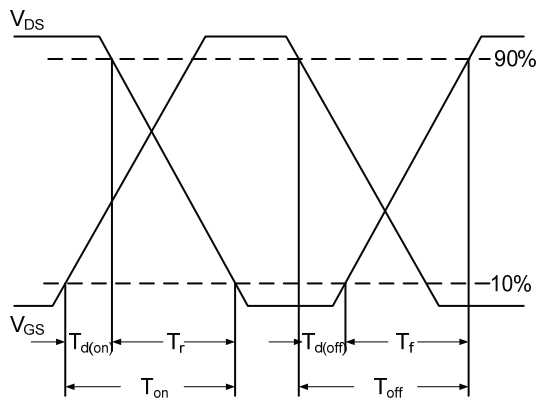


Fig.10 Switching Time Waveform

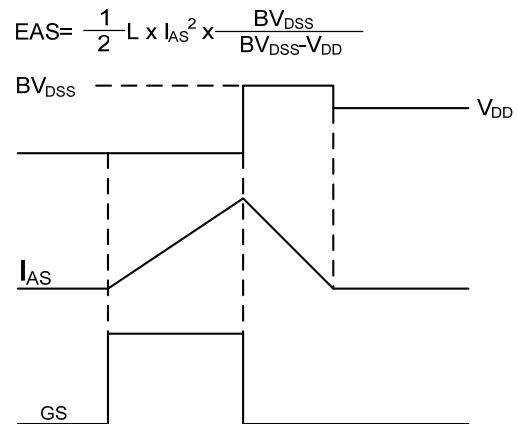
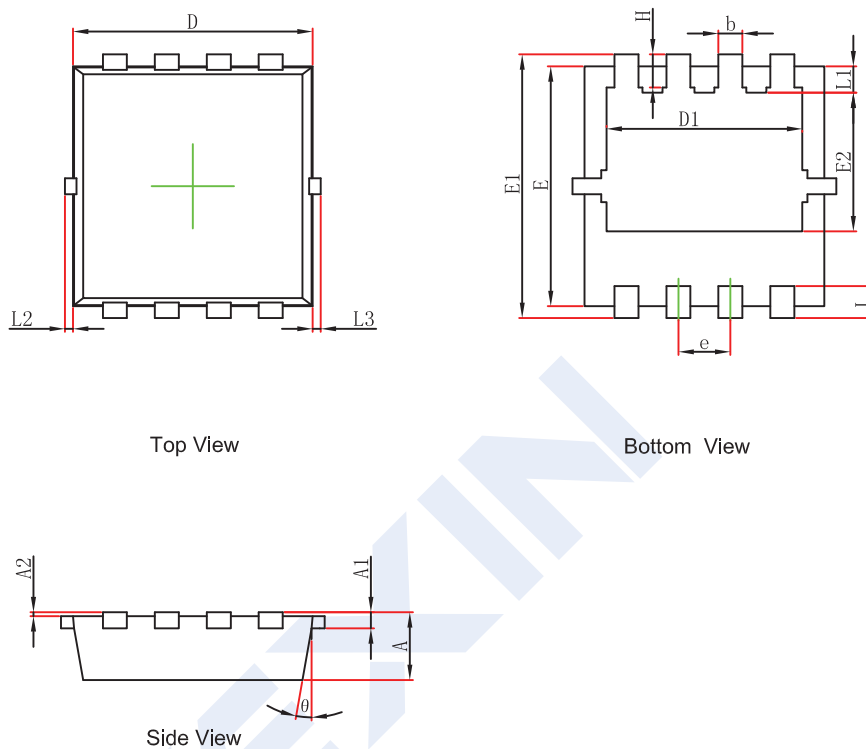


Fig.11 Unclamped Inductive Switching Waveform

## N-Channel MOSFET

## KI60N03DFN

## ■ PDFN3.3x3.3-8 Package Outline Dimensions



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	0.650	0.850	0.026	0.033
A1	0.152 REF.		0.006 REF.	
A2	0~0.05		0~0.002	
D	2.900	3.100	0.114	0.122
D1	2.300	2.600	0.091	0.102
E	2.900	3.100	0.114	0.122
E1	3.150	3.450	0.124	0.136
E2	1.535	1.935	0.060	0.076
b	0.200	0.400	0.008	0.016
e	0.550	0.750	0.022	0.030
L	0.300	0.500	0.012	0.020
L1	0.180	0.480	0.007	0.019
L2	0~0.100		0~0.004	
L3	0~0.100		0~0.004	
H	0.315	0.515	0.012	0.020
$\theta$	9°	13°	9°	13°