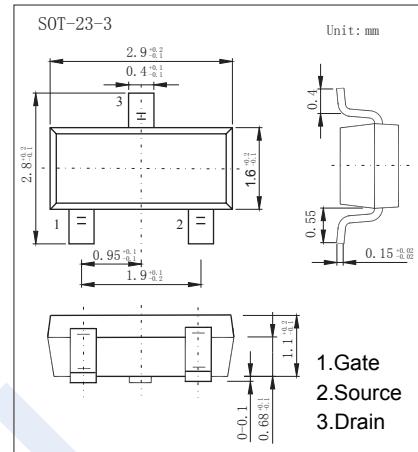
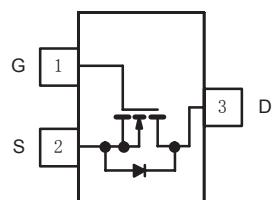


N-Channel MOSFET

KI3305DS

■ Features

- $V_{DS} = 60V$
 - $I_D = 2.3 A$ ($V_{GS} = 10V$)
 - $R_{DS(ON)} < 156m\Omega$ ($V_{GS} = 10V$)
 - $R_{DS(ON)} < 192m\Omega$ ($V_{GS} = 4.5V$)



■ Absolute Maximum Ratings $T_a = 25^\circ\text{C}$

Parameter	Symbol	Rating	Unit	
Drain-Source Voltage	V _{DS}	60	V	
Gate-Source Voltage	V _{GС}	±20		
Continuous Drain Current	T _C =25°C	ID	2.3	
	T _C =70°C		1.8	
	T _A =25°C		1.9	
	Note.1 T _A =70°C		1.5	
			A	
Pulsed Drain Current	I _{DM}	8		
Avalanche Current	L=0.1mH	I _{AS}	6	
Single-Pulse Avalanche Energy		E _{AS}	1.8	
Power Dissipation	T _C =25°C	P _D	1.66	
	T _C =70°C		1.06	
	T _A =25°C		1.09	
	Note.1 T _A =70°C		0.7	
			W	
Thermal Resistance.Junction- to-Ambient	t≤5s	R _{thJA}	115	°C/W
Thermal Resistance.Junction- to-Foot	Steady State	R _{thJF}	75	
Junction Temperature	T _J	150	°C	
Storage Temperature Range	T _{stg}	-55 to 150		

Note.1:Surface Mounted on 1" x 1" FR4 board($t = 5$ s).

N-Channel MOSFET

KI3305DS

■ Electrical Characteristics $T_a = 25^\circ\text{C}$

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Drain-Source Breakdown Voltage	V_{DSS}	$I_D=250 \mu\text{A}, V_{GS}=0\text{V}$	60			V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=60\text{V}, V_{GS}=0\text{V}$			1	μA
		$V_{DS}=60\text{V}, V_{GS}=0\text{V}, T_J=55^\circ\text{C}$			10	
Gate-Body Leakage Current	I_{GSS}	$V_{DS}=0\text{V}, V_{GS}=\pm 20\text{V}$			± 100	nA
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=250 \mu\text{A}$	1		3	V
Static Drain-Source On-Resistance	$R_{DS(on)}$	$V_{GS}=10\text{V}, I_D=1.9\text{A}$ (Note.1)		130	156	$\text{m}\Omega$
		$V_{GS}=4.5\text{V}, I_D=1.7\text{A}$ (Note.1)		160	192	
On State Drain Current	$I_{D(on)}$	$V_{GS}=5\text{V}, V_{DS}=10\text{V}$ (Note.1)	8			A
Forward Transconductance	g_{FS}	$V_{DS}=15\text{V}, I_D=1.9\text{A}$ (Note.1)		5		S
Input Capacitance	C_{iss}	$V_{GS}=0\text{V}, V_{DS}=30\text{V}, f=1\text{MHz}$		190		pF
Output Capacitance	C_{oss}			26		
Reverse Transfer Capacitance	C_{rss}			15		
Gate Resistance	R_g	$V_{GS}=0\text{V}, V_{DS}=0\text{V}, f=1\text{MHz}$	0.6	2.8	5.6	Ω
Total Gate Charge	Q_g	$V_{GS}=30\text{V}, V_{DS}=10\text{V}, I_D=1.9\text{A}$		4.5	6.8	nC
				2.3	3.5	
Gate Source Charge	Q_{gs}	$V_{GS}=30\text{V}, V_{DS}=4.5\text{V}, I_D=1.9\text{A}$		0.8		
Gate Drain Charge	Q_{gd}			1		
Turn-On Delay Time	$t_{d(on)}$	$V_{GS}=10\text{V}, V_{DS}=30\text{V}, R_L=20\Omega, R_G=1\Omega, I_D=1.5\text{A}$		4	6	ns
Turn-On Rise Time	t_r			10	15	
Turn-Off Delay Time	$t_{d(off)}$			10	15	
Turn-Off Fall Time	t_f			7	10.5	
Turn-On Delay Time	$t_{d(on)}$	$V_{GS}=4.5\text{V}, V_{DS}=30\text{V}, R_L=20\Omega, R_G=1\Omega, I_D=1.5\text{A}$		15	23	
Turn-On Rise Time	t_r			16	24	
Turn-Off Delay Time	$t_{d(off)}$			11	17	
Turn-Off Fall Time	t_f			11	17	
Body Diode Reverse Recovery Time	t_{rr}	$I_F= 1.5\text{A}, dI/dt= 100\text{A}/\mu\text{s}, T_J=25^\circ\text{C}$		15	23	nC
Body Diode Reverse Recovery Charge	Q_{rr}			10	15	
Reverse Recovery Fall Time	t_a			12		
Reverse Recovery Rise Time	t_b			3		
Maximum Body-Diode Continuous Current	I_S	$T_c = 25^\circ\text{C}$			1.39	A
Pulse Diode Forward Current	I_{SM}				8	
Diode Forward Voltage	V_{SD}	$I_S=1.5\text{A}, V_{GS}=0\text{V}$		0.8	1.2	V

Note.1: Pulse test; pulse width $\leq 300 \mu\text{s}$, duty cycle $\leq 2\%$.

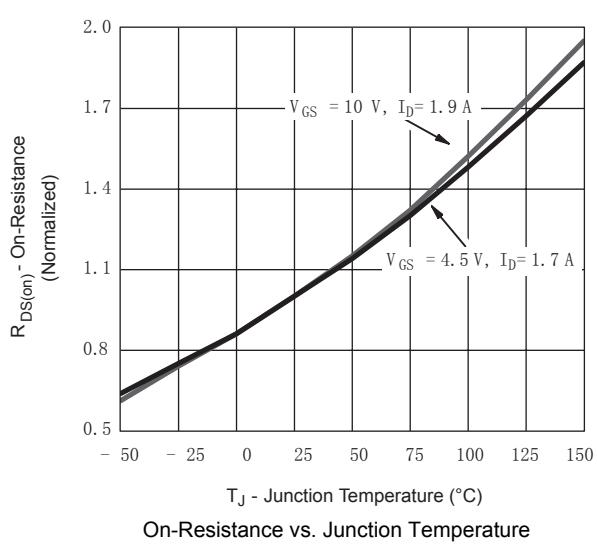
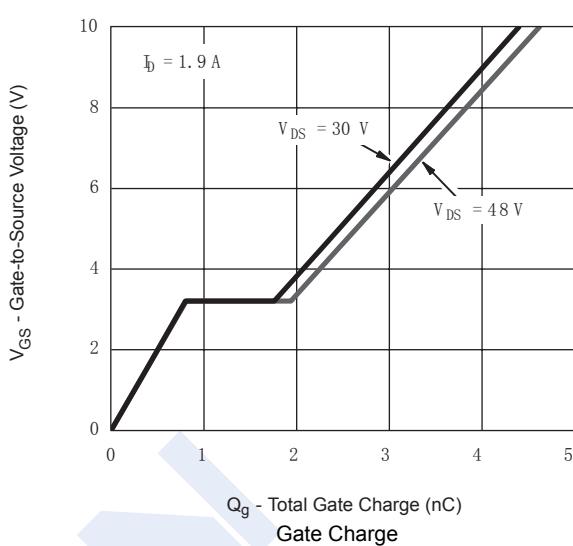
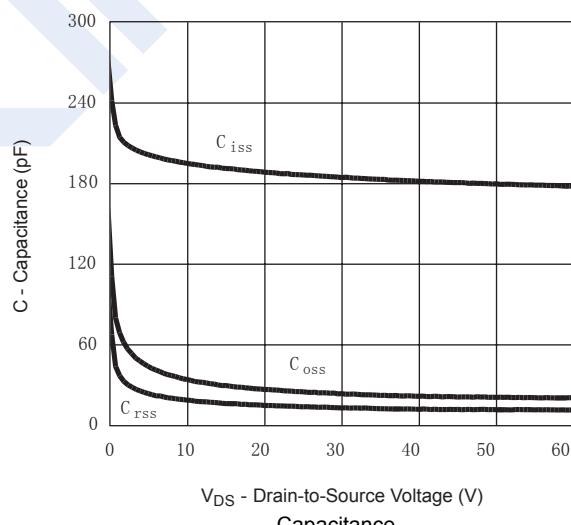
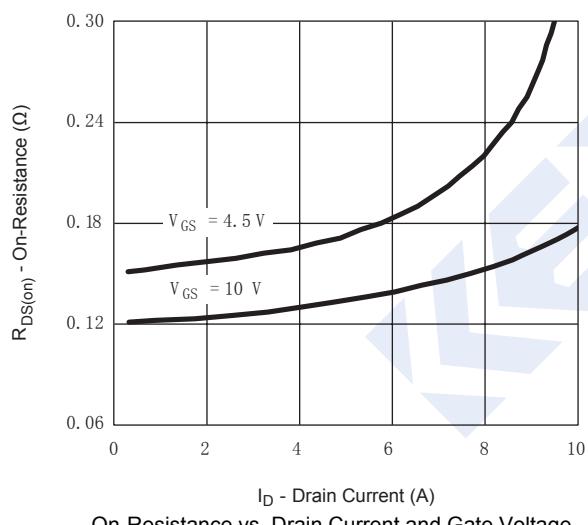
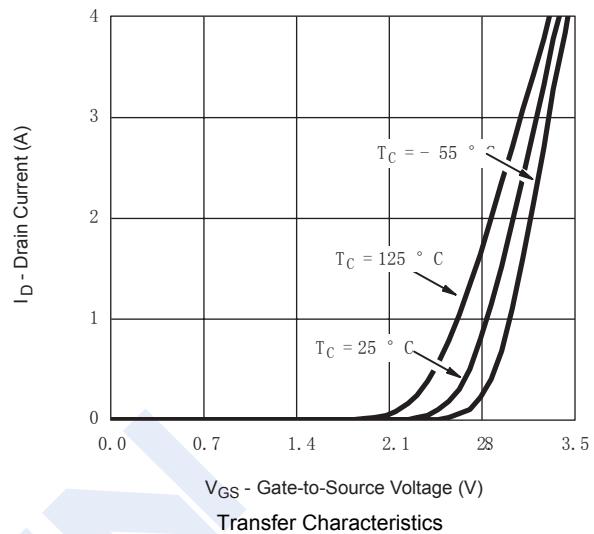
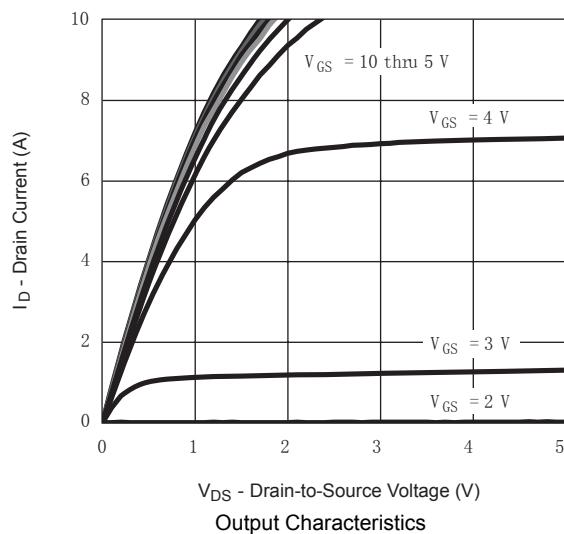
■ Marking

Marking	3055
---------	------

N-Channel MOSFET

KI3305DS

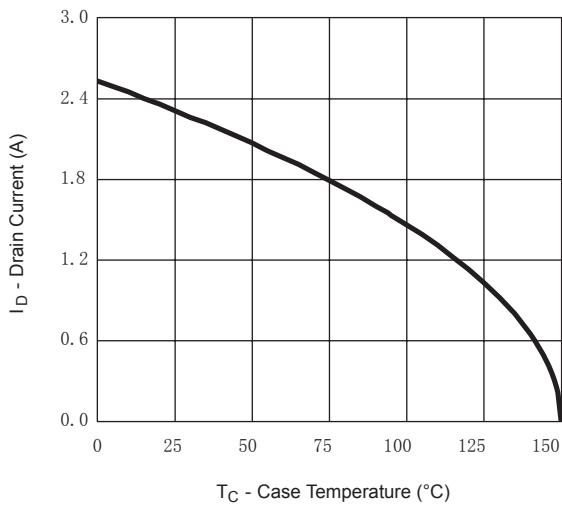
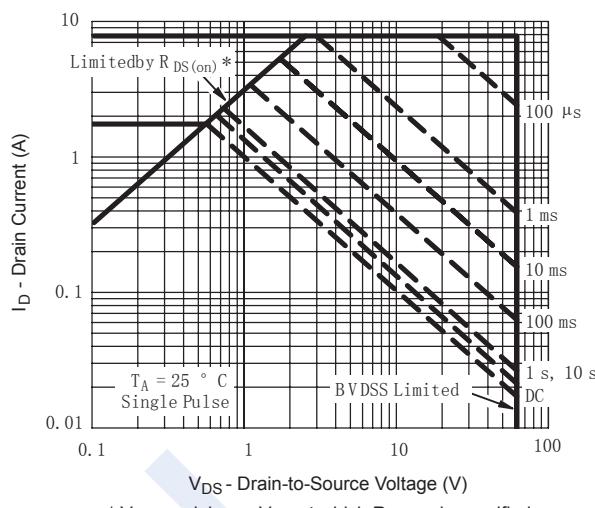
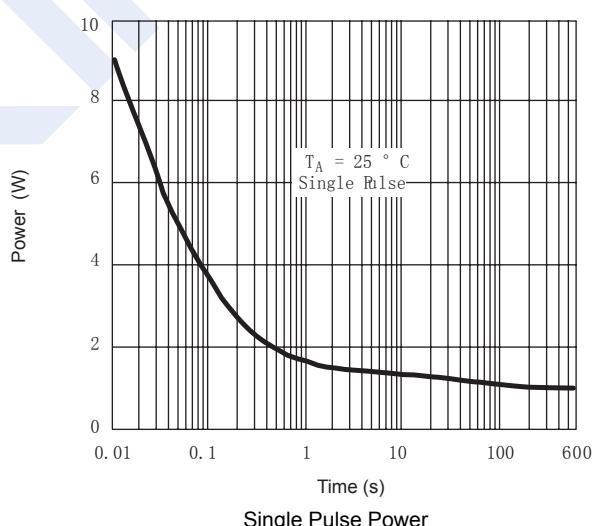
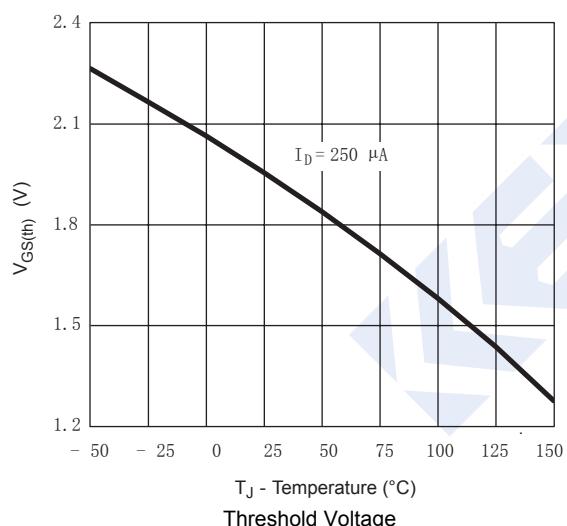
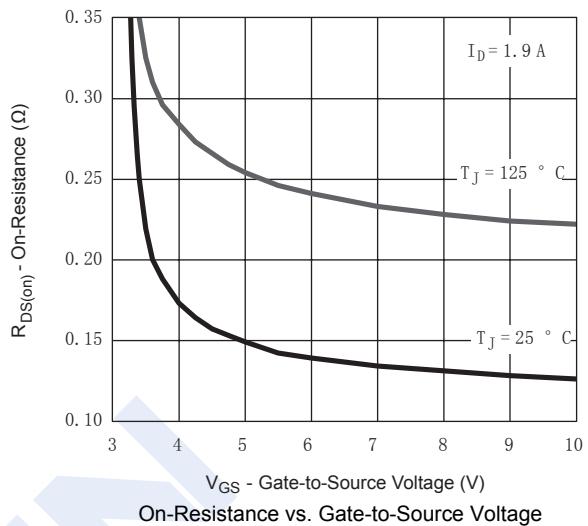
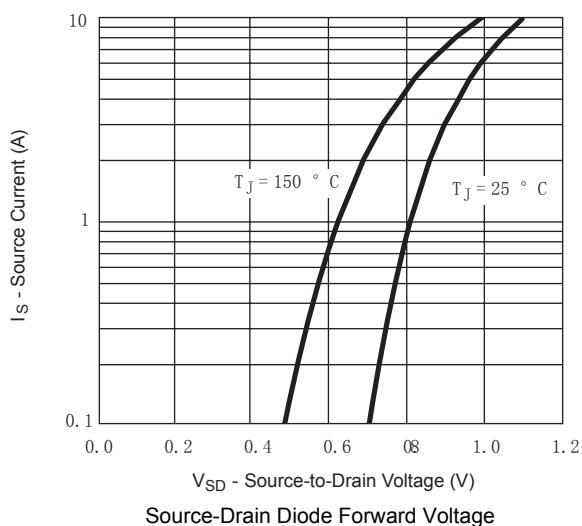
■ Typical Characteristics



N-Channel MOSFET

KI3305DS

■ Typical Characteristics

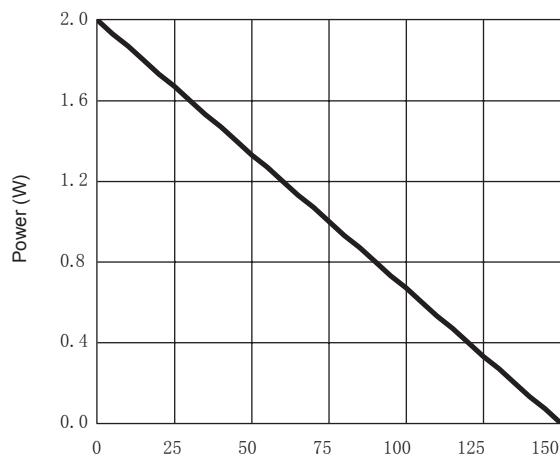


* $V_{GS} >$ minimum V_{GS} at which $R_{DS(on)}$ is specified
Safe Operating Area

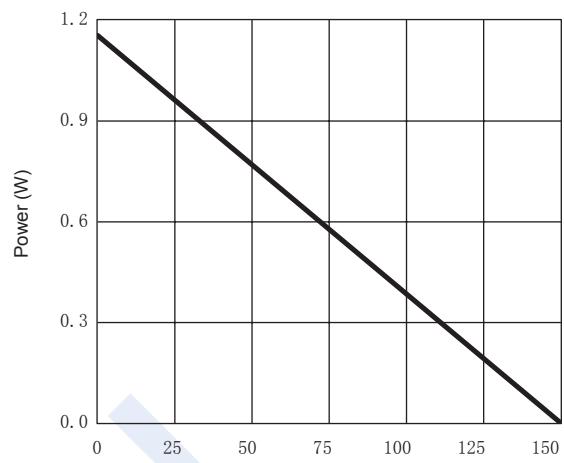
N-Channel MOSFET

KI3305DS

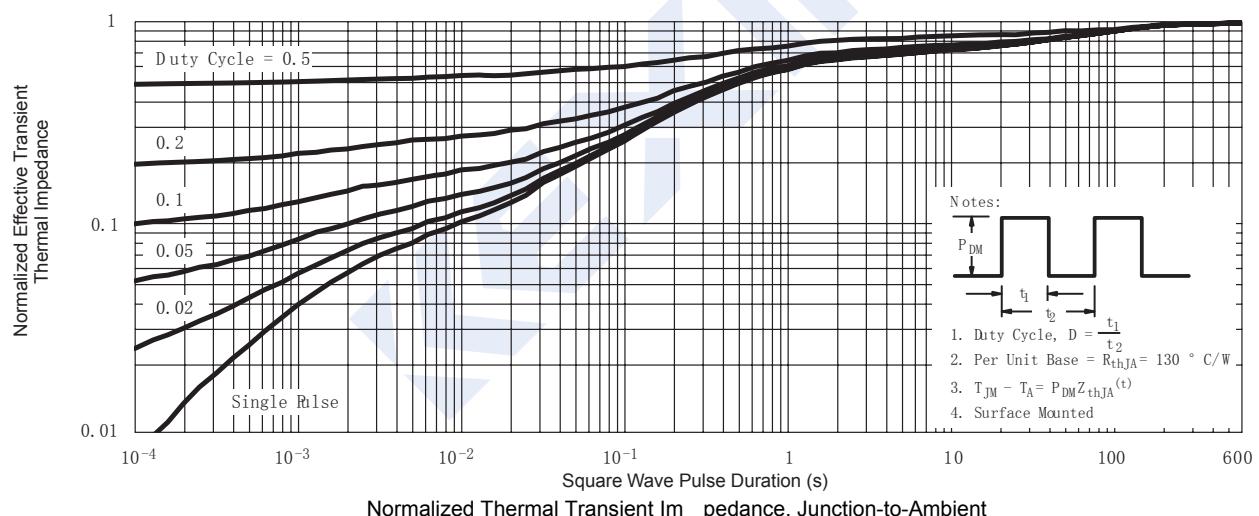
■ Typical Characteristics



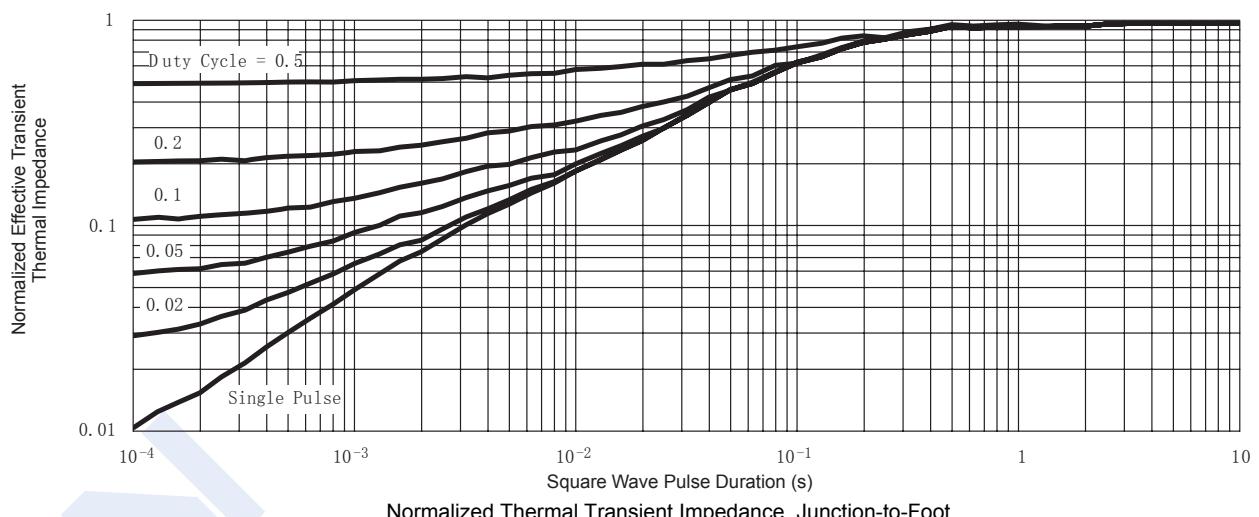
T_c - Case Temperature (°C)
Power Derating, Junction-to-Case



T_A - Ambient Temperature (°C)
Power Derating, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Foot