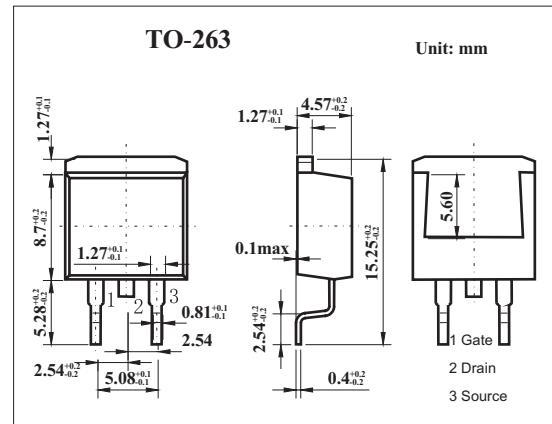
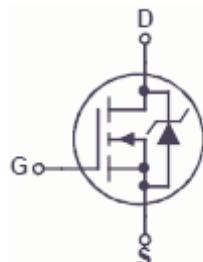


N-Channel PowerTrench MOSFET

KDB3632(FDB3632)

■ Features

- $r_{DS(ON)} = 7.5\text{m}\Omega$ (Typ.), $V_{GS} = 10\text{V}$, $I_D = 80\text{A}$
- $Q_{G(\text{tot})} = 84\text{nC}$ (Typ.), $V_{GS} = 10\text{V}$
- Low Miller Charge
- Low QRR Body Diode
- UIS Capability (Single Pulse and Repetitive Pulse)



■ Absolute Maximum Ratings $T_a = 25^\circ\text{C}$

Parameter	Symbol	Rating	Unit
Drain to source voltage	V_{DSS}	100	V
Gate to source voltage	V_{GSS}	± 20	V
Drain current-Continuous $T_c < 111^\circ\text{C}$ $T_A = 25^\circ\text{C}$	I_D	80	A
		12	A
Power dissipation Derate above 25°C	P_D	310	W
		2.07	W/ $^\circ\text{C}$
Thermal Resistance Junction to Ambient	$R_{\theta JA}$	43	$^\circ\text{C}/\text{W}$
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	0.48	$^\circ\text{C}/\text{W}$
Channel temperature	T_{ch}	175	$^\circ\text{C}$
Storage temperature	T_{stg}	-55 to +175	$^\circ\text{C}$

KDB3632(FDB3632)■ Electrical Characteristics $T_a = 25^\circ\text{C}$

Parameter	Symbol	Testconditons	Min	Typ	Max	Unit
Drain to source breakdown voltage	V_{DSS}	$I_D=250\mu\text{A}, V_{GS}=0\text{V}$	100			V
Drain cut-off current	I_{DSS}	$V_{DS}=80\text{V}, V_{GS}=0$			1	μA
		$V_{DS}=80\text{V}, V_{GS}=0, T_c=150^\circ\text{C}$			250	μA
Gate leakage current	I_{GSS}	$V_{GS}=\pm 20\text{V}$			± 100	nA
Gate threshold voltage	$V_{GS(\text{th})}$	$V_{DS} = V_{GS}, I_D = 250\mu\text{A}$	2.0		4.0	V
Drain to source on-state resistance	$R_{DS(on)}$	$V_{GS}=10\text{V}, I_D=80\text{A}$		0.0075	0.009	Ω
		$V_{GS}=6\text{V}, I_D=40\text{A}$		0.009	0.015	
		$V_{GS}=10\text{V}, I_D=80\text{A}, T_c=175^\circ\text{C}$		0.018	0.022	
Input capacitance	C_{iss}	$V_{DS}=25\text{V}, V_{GS}=0, f=1\text{MHZ}$		6000		pF
Output capacitance	C_{oss}			820		pF
Reverse transfer capacitance	C_{rss}			200		pF
Total Gate Charge at 10V	$Q_g(\text{TOT})$	$V_{GS} = 0\text{V} \text{ to } 10\text{V}$		84	110	nC
Threshold Gate Charge	$Q_g(\text{TH})$	$V_{GS} = 0\text{V} \text{ to } 2\text{V}$		11	14	nC
Gate to Source Gate Charge	Q_{gs}	$V_{DS} = 50\text{ V}, I_D = 80\text{A}, I_g=1.0\text{mA}$		30		nC
Gate Charge Threshold to Plateau	Q_{gs2}			20		nC
Gate to Drain "Miller" Charge	Q_{gd}			20		nC
Turn-On Time	t_{ON}	$V_{DD} = 50\text{ V}, I_D = 80\text{A}, V_{GS} = 10\text{ V}, R_{GEN} = 3.6\Omega$			102	ns
Turn-On Delay Time	$t_{d(ON)}$				30	ns
Rise Time	t_r				39	ns
Turn-Off Delay Time	$t_{d(OFF)}$				96	ns
Fall Time	t_f				46	ns
Turn-Off Time	t_{OFF}				213	ns
Source to Drain Diode Voltage	V_{SD}	$I_{SD}=80\text{A}$			1.25	V
		$I_{SD}=40\text{A}$			1.0	V
Reverse Recovery Time	t_{rr}	$I_{SD} = 75\text{A}, dI_{SD}/dt = 100\text{A}/\mu\text{s}$			64	ns
Reverse Recovered Charge	Q_{RR}	$I_{SD} = 75\text{A}, dI_{SD}/dt = 100\text{A}/\mu\text{s}$			120	nC