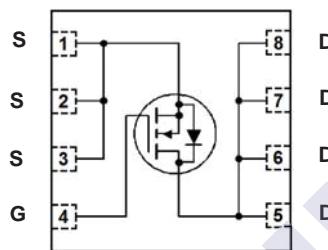


N-Channel MOSFET

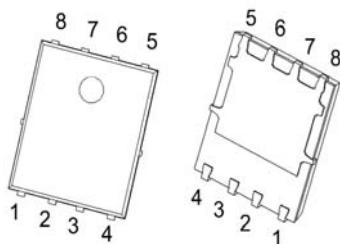
2KK5046DFN

■ Features

- V_{DS} (V) = 40 V
- I_D = 200 A
- $R_{DS(ON)}$ (at V_{GS} = 10 V) < 1.4 mΩ



DFN5x6-8(PDFNWB5x6-8L)

■ Absolute Maximum Ratings (T_c = 25°C unless otherwise noted)

Parameter		Symbol	Rating	Unit
Drain-Source Voltage	T_c = 25°C	V_{DS}	40	V
Gate-Source Voltage		V_{GS}	± 20	
Continuous Drain Current	T_c = 100°C	I_D	200	A
			130	
Pulsed Drain Current (Note 1)	T_c	I_{DM}	800	
Power Dissipation		P_D	92.6	
Power Dissipation – Derate above 25°C			0.74	W/°C
Single Pulse Avalanche Energy (Note 2)		E_{AS}	450	mJ
Single Pulse Avalanche Current (Note 2)		I_{AS}	30	A
Thermal Resistance, Junction- to-Ambient		R_{JA}	62	°C/W
Thermal Resistance, Junction- to-Case		R_{JC}	1.35	
Junction Temperature		T_J	150	°C
Storage Temperature Range		T_{STG}	-55 to 150	

Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. EAS condition : $T_j=25^\circ\text{C}$, $V_{DD}=25\text{V}$, $V_G=10\text{V}$, $L=1\text{mH}$, $I_{AS}=30\text{A}$

N-Channel MOSFET

2KK5046DFN

■ Electrical Characteristics ($T_c = 25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV_{DSS}	$\text{Id} = 250 \mu\text{A}, \text{V}_{\text{GS}} = 0\text{V}$	40			V
Zero Gate Voltage Drain Current	I_{DSS}	$\text{V}_{\text{DS}} = 40 \text{ V}, \text{V}_{\text{GS}} = 0 \text{ V}$			1	μA
		$\text{V}_{\text{DS}} = 32 \text{ V}, \text{V}_{\text{GS}} = 0 \text{ V}, \text{T}_j=125^\circ\text{C}$			10	
Gate to Source Leakage Current	I_{GSS}	$\text{V}_{\text{DS}} = 0 \text{ V}, \text{V}_{\text{GS}} = \pm 20 \text{ V}$			± 100	nA
On Characteristics (Note 1)						
Gate to Source Threshold Voltage	$\text{V}_{\text{GS(th)}}$	$\text{V}_{\text{DS}} = \text{V}_{\text{GS}}, \text{Id} = 250\mu\text{A}$	1.0		3.0	V
Static Drain-Source On-Resistance	$\text{R}_{\text{DS(on)}}$	$\text{V}_{\text{GS}} = 10 \text{ V}, \text{Id} = 50 \text{ A}$			1.4	$\text{m}\Omega$
Forward Transconductance	g_{FS}	$\text{V}_{\text{DS}} = 10 \text{ V}, \text{Id} = 50 \text{ A}$		120		S
Dynamic Characteristics (Note 1)						
Input Capacitance	C_{iss}	$\text{V}_{\text{GS}} = 0 \text{ V}, \text{V}_{\text{DS}} = 20 \text{ V}, \text{f} = 1 \text{ MHz}$		7500		pF
Output Capacitance	C_{oss}			230		
Reverse Transfer Capacitance	C_{rss}			3.2		
Gate Resistance	R_g	$\text{V}_{\text{GS}}=0\text{V}, \text{V}_{\text{DS}}=0\text{V}, \text{F}=1\text{MHz}$		1.4	2.8	Ω
Switching Characteristics (Note 1)						
Total Gate Charge	Q_g	$\text{V}_{\text{GS}} = 10 \text{ V}, \text{V}_{\text{DS}} = 20 \text{ V}, \text{Id} = 50 \text{ A}$		115		nC
Gate Source Charge	Q_{gs}			24		
Gate Drain Charge	Q_{gd}			19		
Turn-On Delay Time	$\text{t}_{\text{d(on)}}$	$\text{V}_{\text{GS}} = 10 \text{ V}, \text{V}_{\text{DD}} = 20 \text{ V}, \text{Id} = 50 \text{ A}, \text{RG} = 3 \Omega$		20		ns
Turn-On Rise Time	t_r			32		
Turn-Off Delay Time	$\text{t}_{\text{d(off)}}$			98		
Turn-Off Fall Time	t_f			32		
Drain-Source Diode Characteristics						
Body Diode Reverse Recovery Time	t_{rr}	$\text{I}_{\text{s}} = 50 \text{ A}, \text{di/dt} = 100 \text{ A}/\mu\text{s}$		64		ns
Body Diode Reverse Recovery Charge	Q_{rr}			98		
Maximum Body-Diode Continuous Current	I_{s}	$\text{VG}=\text{VD}=0\text{V}, \text{Force Current}$			200	A
Diode Forward Voltage	V_{SD}	$\text{V}_{\text{GS}} = 0 \text{ V}, \text{I}_{\text{s}} = 1 \text{ A}$			1	V

Notes:

1. Pulse Test: Pulse Width $\leqslant 300\mu\text{s}$, Duty Cycle $\leqslant 2\%$.

■ Marking

Marking	K5046 KC****
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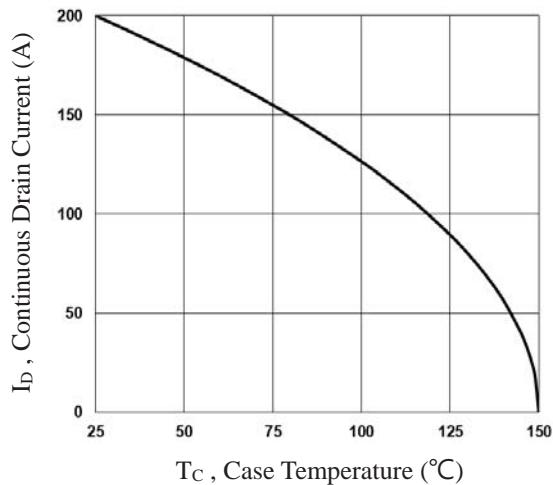
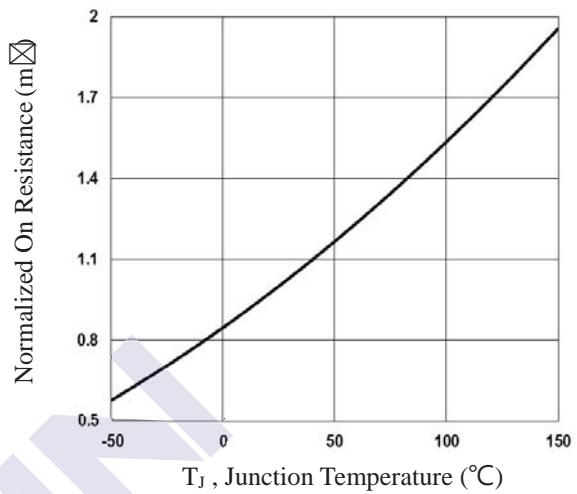
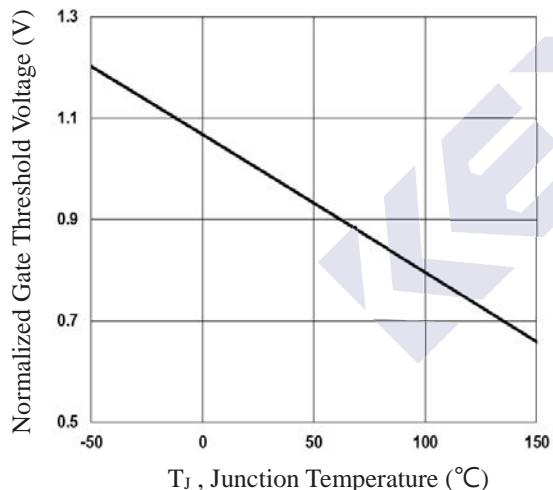
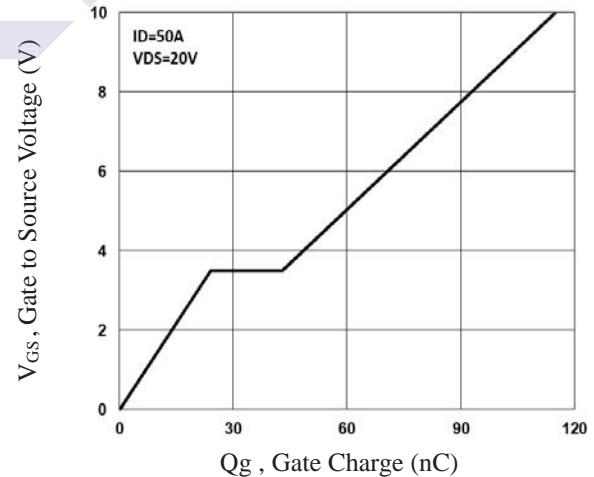
N-Channel MOSFET**2KK5046DFN****■ Typical Electrical Characteristics**Fig.1 Continuous Drain Current vs. T_C Fig.2 Normalized $R_{DS(on)}$ vs. T_J Fig.3 Normalized V_{th} vs. T_J 

Fig.4 Gate Charge Characteristics

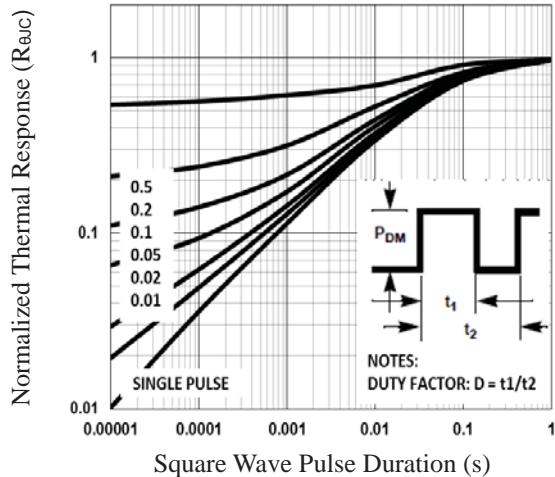
N-Channel MOSFET**2KK5046DFN**

Fig.5 Normalized Transient Impedance

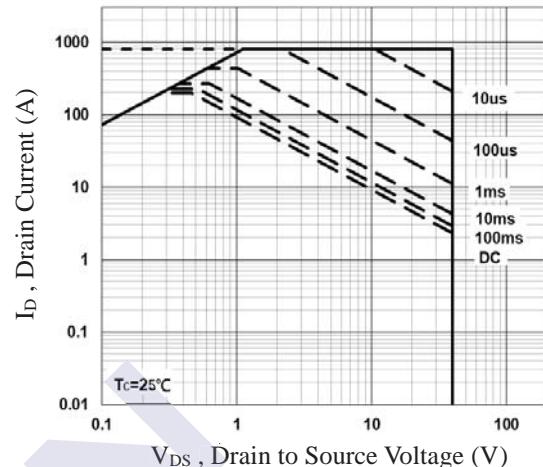


Fig.6 Maximum Safe Operation Area

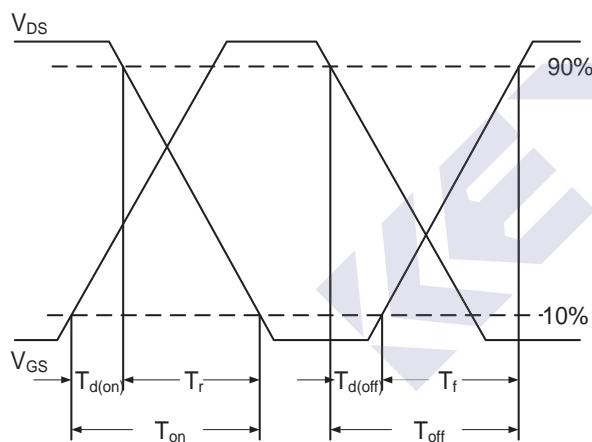


Fig.7 Switching Time Waveform

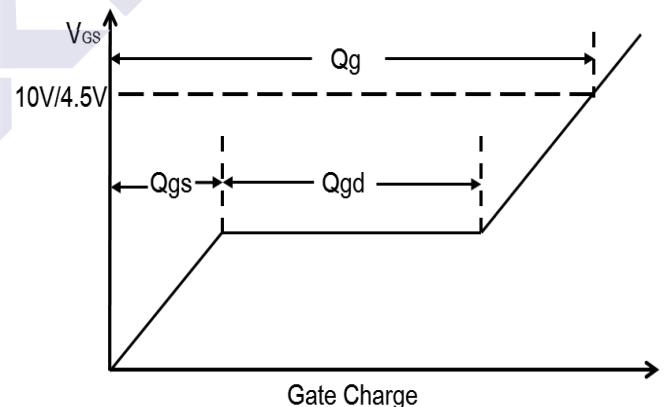
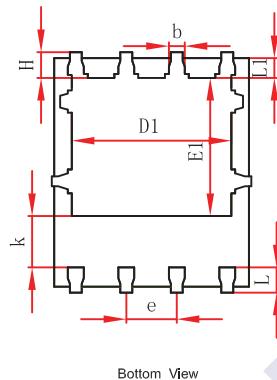
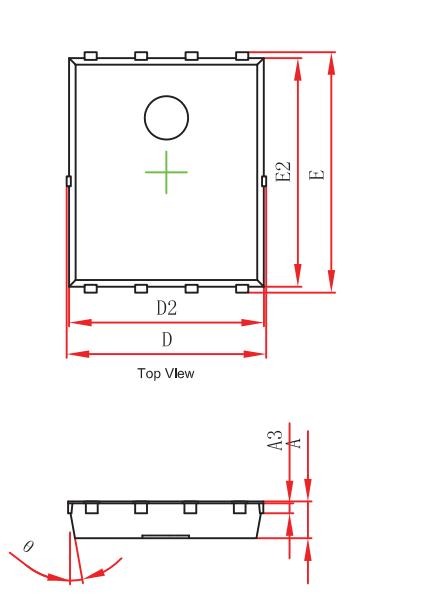


Fig.8 Gate Charge Waveform

N-Channel MOSFET

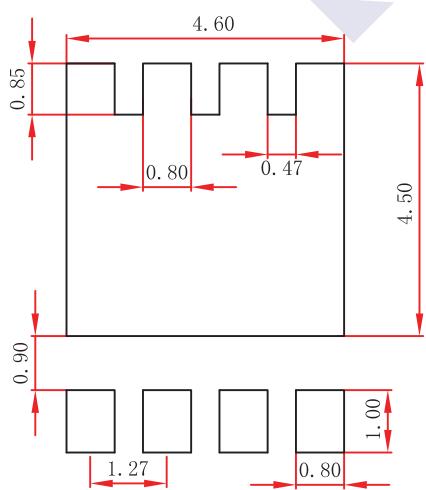
2KK5046DFN

■ DFN5x6-8(PDFNWB5x6-8L) Package Outline Dimensions



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	0.900	1.000	0.035	0.039
A3	0.254REF.		0.010REF.	
D	4.944	5.096	0.195	0.201
E	5.974	6.126	0.235	0.241
D1	3.910	4.110	0.154	0.162
E1	3.375	3.575	0.133	0.141
D2	4.824	4.976	0.190	0.196
E2	5.674	5.826	0.223	0.229
k	1.190	1.390	0.047	0.055
b	0.350	0.450	0.014	0.018
e	1.270TYP.		0.050TYP.	
L	0.559	0.711	0.022	0.028
L1	0.424	0.576	0.017	0.023
H	0.574	0.726	0.023	0.029
θ	10°	12°	10°	12°

■ DFN5x6-8(PDFNWB5x6-8L) Suggested Pad Layout



Note:

1. Controlling dimension: in millimeters.
2. General tolerance: ± 0.05 mm.
3. The pad layout is for reference purposes only.