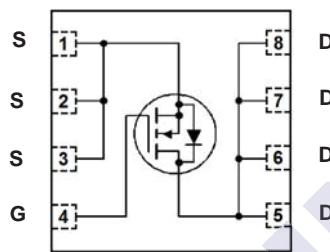


N-Channel MOSFET

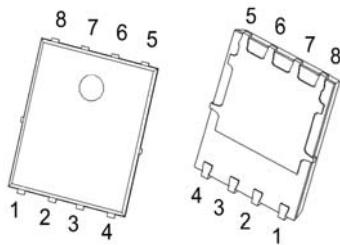
2KK5041DFN

■ Features

- V_{DS} (V) = 100 V
- I_D = 60 A
- $R_{DS(ON)}$ (at V_{GS} = 10 V) < 8.8 mΩ
- $R_{DS(ON)}$ (at V_{GS} = 4.5 V) < 11.5 mΩ



DFN5x6-8(PDFNW5x6-8L)

■ Absolute Maximum Ratings (T_c = 25°C unless otherwise noted)

Parameter		Symbol	Rating	Unit
Drain-Source Voltage		V_{DS}	100	V
Gate-Source Voltage		V_{GS}	± 20	
Continuous Drain Current	Package Limited $T_c = 100^\circ\text{C}$	I_D	60	A
Pulsed Drain Current (Note 1)			42.5	
Power Dissipation		P_D	105	W
Derating factor			0.84	W/°C
Single Pulse Avalanche Energy (Note 2)		E_{AS}	250	mJ
Thermal Resistance Junction-to-Case (Note 3)		$R_{\theta JC}$	1.2	°C/W
Junction Temperature		T_J	150	°C
Storage Temperature Range		T_{stg}	-55 to 150	

Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. EAS condition : $T_j=25^\circ\text{C}$, $V_{DD}=50\text{V}$, $V_G=10\text{V}$, $L=0.5\text{mH}$, $R_g=25\Omega$
3. Surface Mounted on FR4 Board, $t \leqslant 10$ sec.

N-Channel MOSFET

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■ Electrical Characteristics ($T_c = 25^\circ\text{C}$ unless otherwise specified)

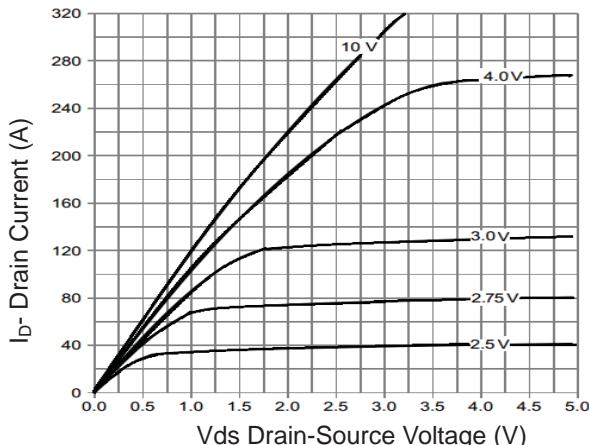
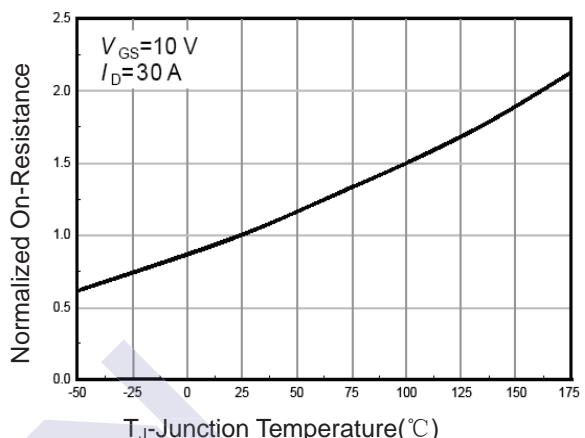
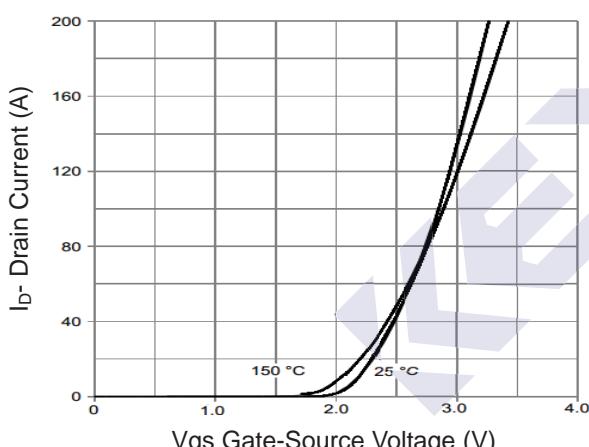
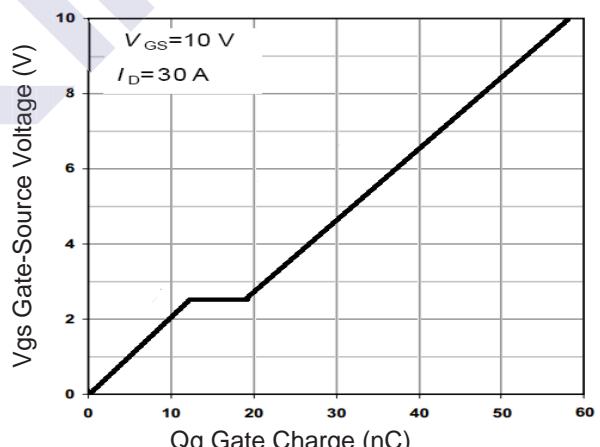
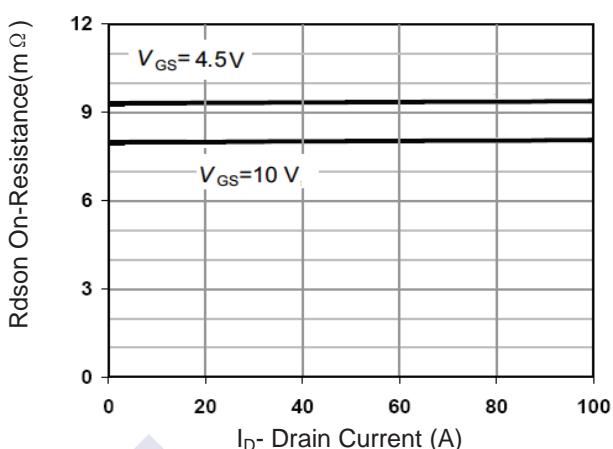
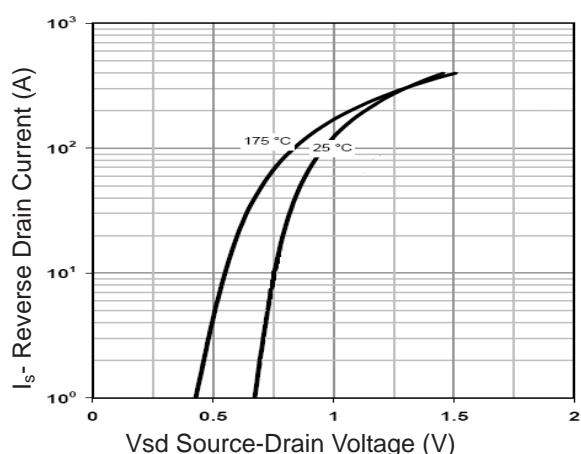
Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV_{DSS}	$\text{ID} = 250 \mu\text{A}, \text{V}_{\text{GS}} = 0 \text{ V}$	100			V
Zero Gate Voltage Drain Current	I_{DSS}	$\text{V}_{\text{DS}} = 100 \text{ V}, \text{V}_{\text{GS}} = 0 \text{ V}$			1	μA
Gate to Source Leakage Current	I_{GSS}	$\text{V}_{\text{DS}} = 0 \text{ V}, \text{V}_{\text{GS}} = \pm 20 \text{ V}$			± 100	nA
On Characteristics (Note 1)						
Gate to Source Threshold Voltage	$\text{V}_{\text{GS(th)}}$	$\text{V}_{\text{DS}} = \text{V}_{\text{GS}}, \text{ID} = 250 \mu\text{A}$	1.0	1.7	2.2	V
Static Drain-Source On-Resistance	$\text{R}_{\text{DS(on)}}$	$\text{V}_{\text{GS}} = 10 \text{ V}, \text{ID} = 30 \text{ A}$			8.8	$\text{m}\Omega$
		$\text{V}_{\text{GS}} = 4.5 \text{ V}, \text{ID} = 30 \text{ A}$			11.5	
Forward Transconductance	g_{FS}	$\text{V}_{\text{DS}} = 10 \text{ V}, \text{ID} = 30 \text{ A}$	40			S
Dynamic Characteristics (Note 1)						
Input Capacitance	C_{iss}	$\text{V}_{\text{GS}} = 0 \text{ V}, \text{V}_{\text{DS}} = 50 \text{ V}, \text{f} = 1 \text{ MHz}$		4200		pF
Output Capacitance	C_{oss}			354		
Reverse Transfer Capacitance	C_{rss}			23		
Switching Characteristics (Note 1)						
Total Gate Charge	Q_g	$\text{V}_{\text{GS}} = 10 \text{ V}, \text{V}_{\text{DS}} = 50 \text{ V}, \text{ID} = 30 \text{ A}$		58		nC
Gate Source Charge	Q_{gs}			12		
Gate Drain Charge	Q_{gd}			7.8		
Turn-On Delay Time	$\text{t}_{\text{d(on)}}$	$\text{V}_{\text{GS}} = 10 \text{ V}, \text{V}_{\text{DD}} = 50 \text{ V}, \text{ID} = 30 \text{ A}, \text{RG} = 4.7 \Omega$		14		ns
Turn-On Rise Time	t_{r}			9		
Turn-Off Delay Time	$\text{t}_{\text{d(off)}}$			39		
Turn-Off Fall Time	t_{f}			5		
Drain-Source Diode Characteristics						
Body Diode Reverse Recovery Time	t_{rr}	$\text{I}_{\text{F}} = \text{I}_{\text{S}}, \frac{d\text{I}}{dt} = 100 \text{ A}/\mu\text{s}, \text{T}_{\text{J}} = 25^\circ\text{C}$		58		ns
Body Diode Reverse Recovery Charge	Q_{rr}			110		
Maximum Body-Diode Continuous Current	I_{S}				60	A
Diode Forward Voltage	V_{SD}	$\text{V}_{\text{GS}} = 0 \text{ V}, \text{I}_{\text{S}} = 60 \text{ A}$			1.2	V

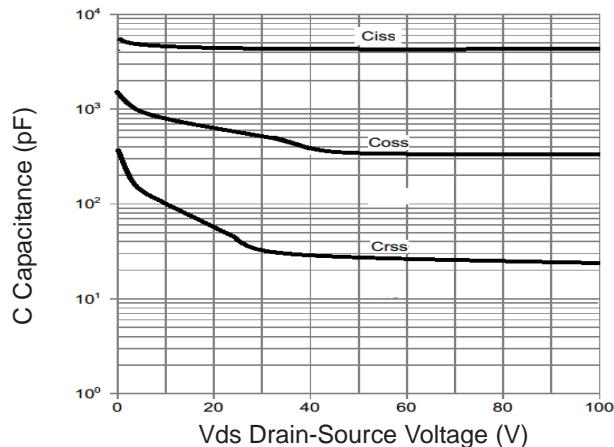
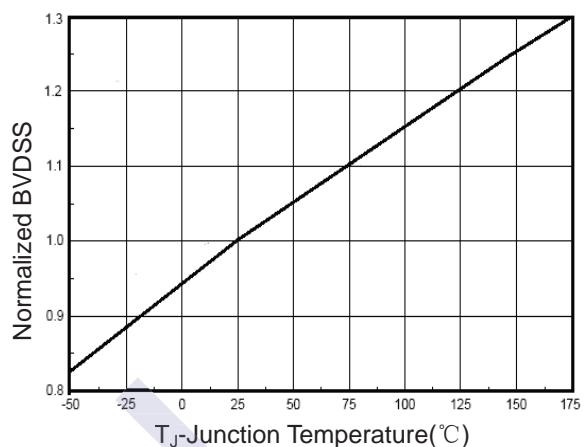
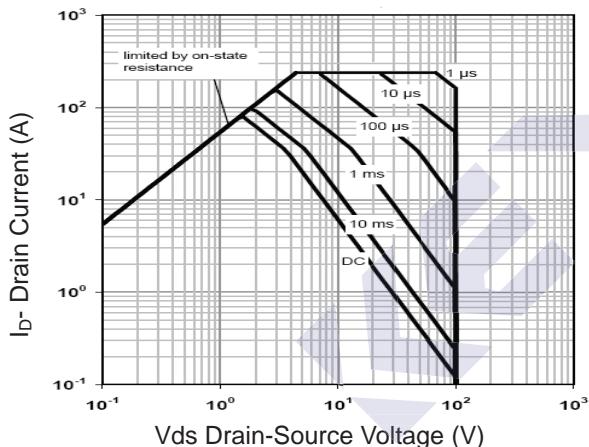
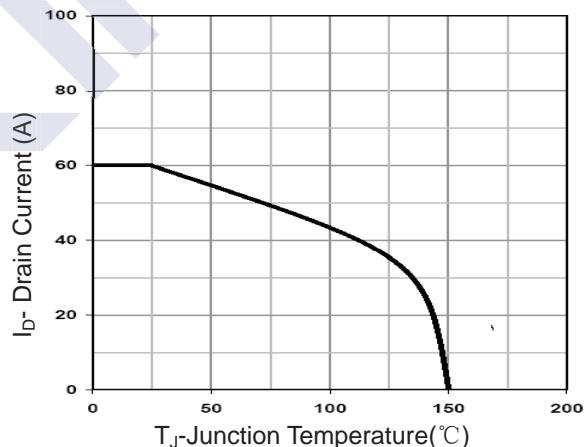
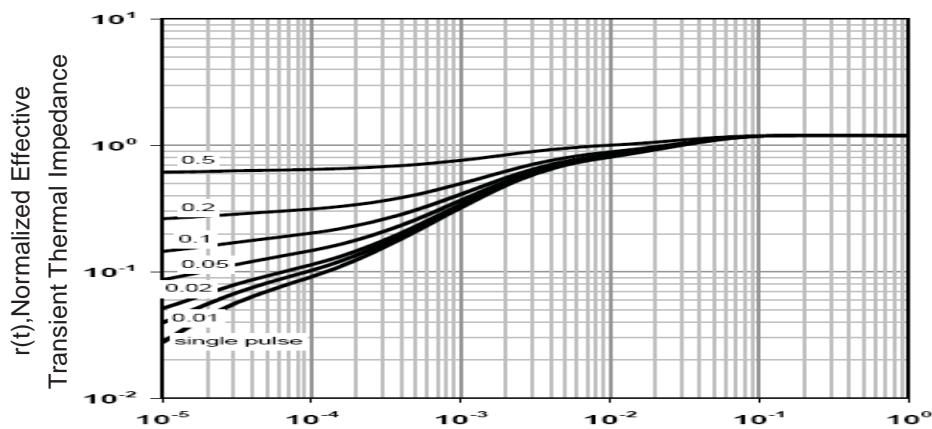
Notes:

1. Pulse Test: Pulse Width $\leq 300\mu\text{s}$, Duty Cycle $\leq 2\%$.

■ Marking

Marking	K5041 KC****
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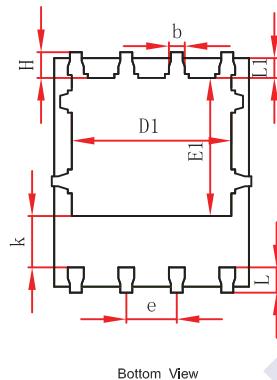
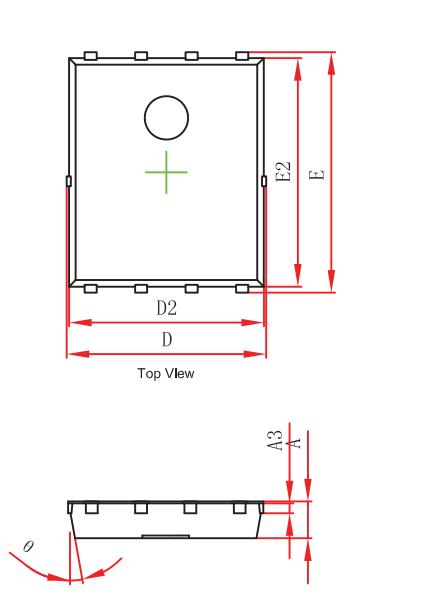
N-Channel MOSFET**2KK5041DFN****■ Typical Electrical and Thermal Characteristics****Figure 1 Output Characteristics****Figure 4 Rdson-JunctionTemperature****Figure 2 Transfer Characteristics****Figure 5 Gate Charge****Figure 3 Rdson- Drain Current****Figure 6 Source- Drain Diode Forward**

N-Channel MOSFET**2KK5041DFN****Figure 7 Capacitance vs Vds****Figure 9 BV_{DSS} vs Junction Temperature****Figure 8 Safe Operation Area****Figure 10 Current De-rating****Figure 11 Normalized Maximum Transient Thermal Impedance**

N-Channel MOSFET

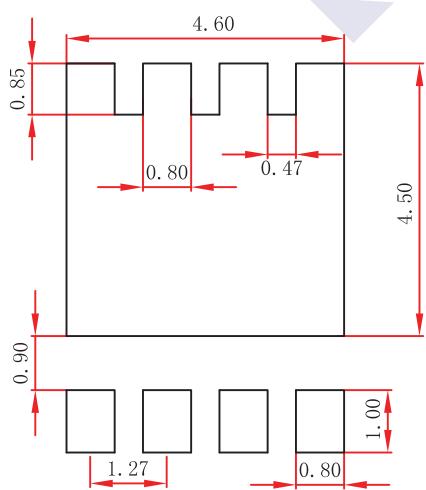
2KK5041DFN

■ DFN5x6-8(PDFNWB5x6-8L) Package Outline Dimensions



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	0.900	1.000	0.035	0.039
A3	0.254REF.		0.010REF.	
D	4.944	5.096	0.195	0.201
E	5.974	6.126	0.235	0.241
D1	3.910	4.110	0.154	0.162
E1	3.375	3.575	0.133	0.141
D2	4.824	4.976	0.190	0.196
E2	5.674	5.826	0.223	0.229
k	1.190	1.390	0.047	0.055
b	0.350	0.450	0.014	0.018
e	1.270TYP.		0.050TYP.	
L	0.559	0.711	0.022	0.028
L1	0.424	0.576	0.017	0.023
H	0.574	0.726	0.023	0.029
θ	10°	12°	10°	12°

■ DFN5x6-8(PDFNWB5x6-8L) Suggested Pad Layout



Note:

1. Controlling dimension: in millimeters.
2. General tolerance: ± 0.05 mm.
3. The pad layout is for reference purposes only.